

Fig. 1

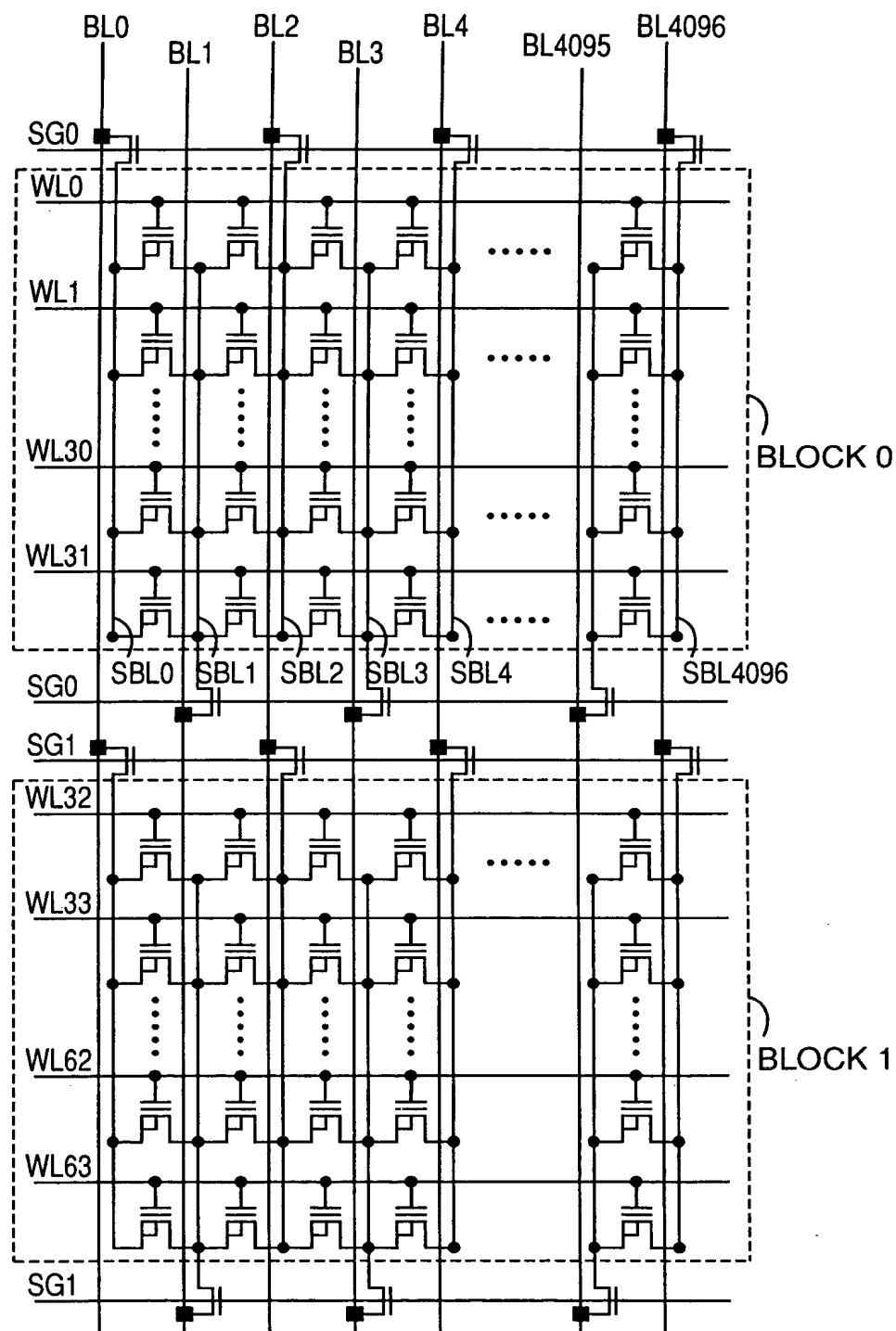


Fig.2

CONDITIONS : $V_g = -8V$, $V_d = -8V$, $V_s = -8V$, $V_{sub} = -8V$

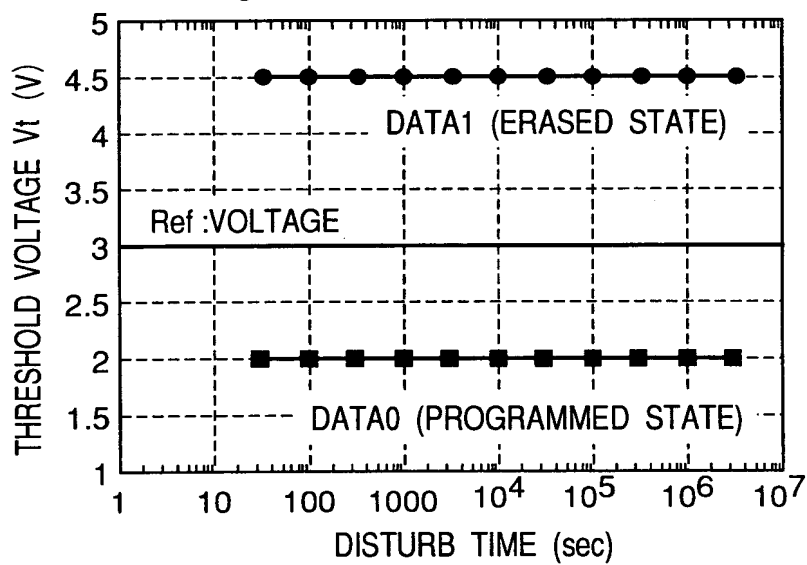


Fig.3

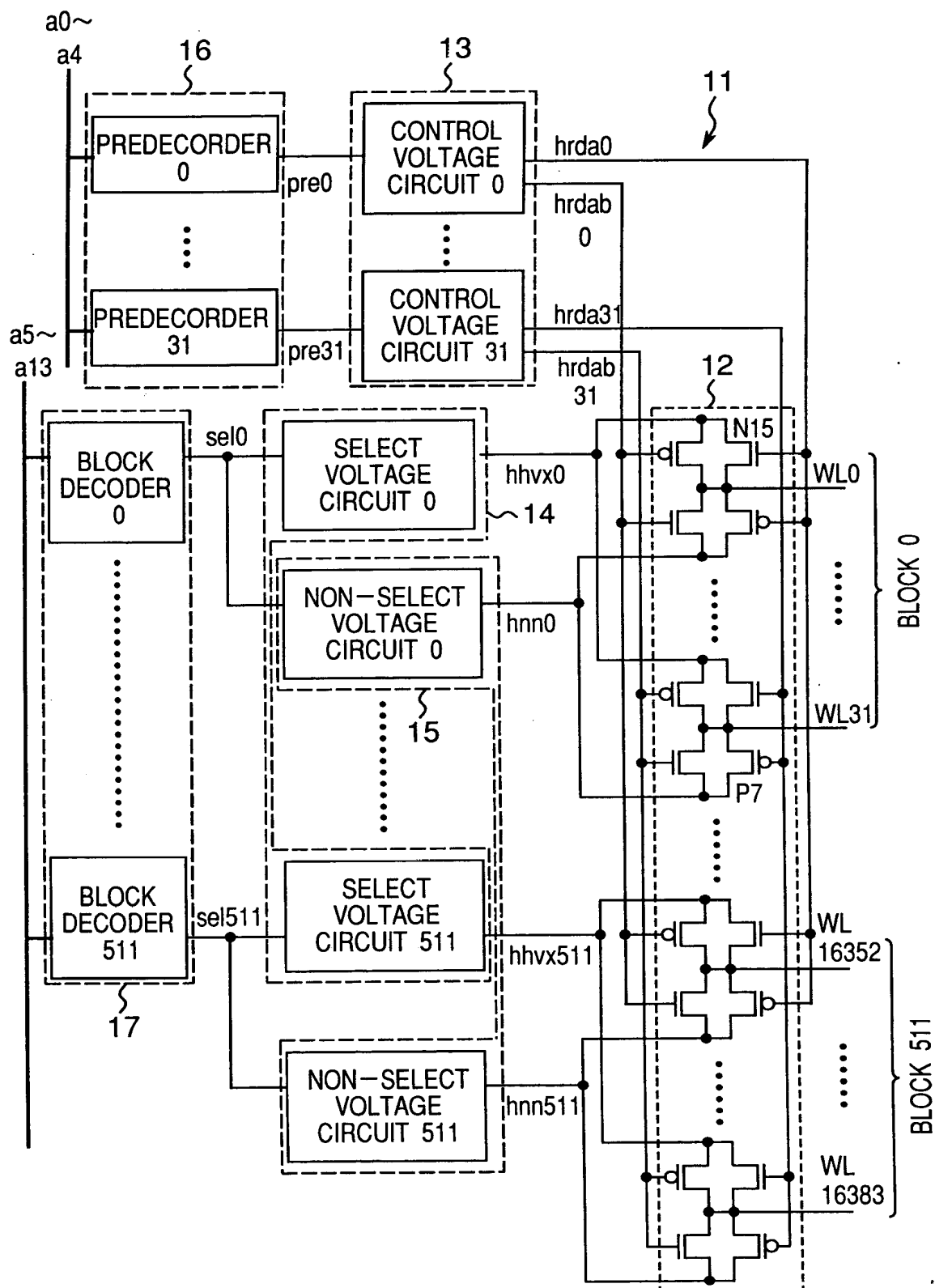


Fig.4

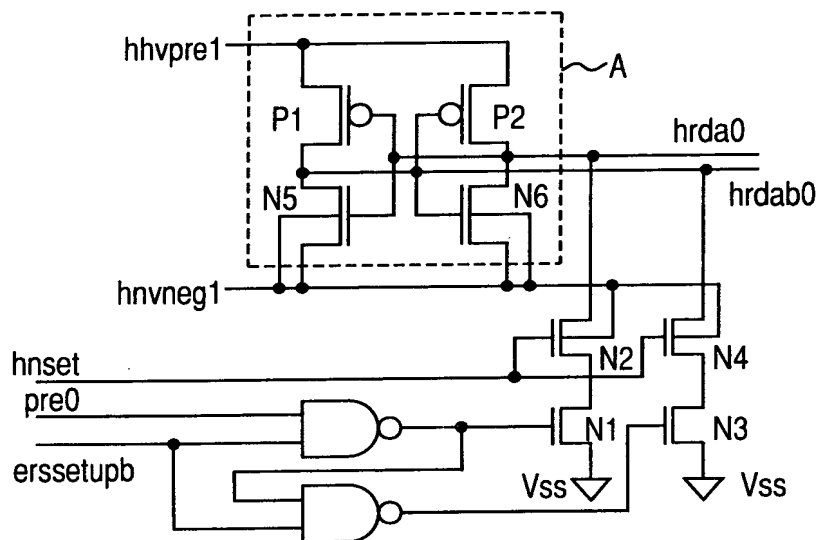


Fig.5

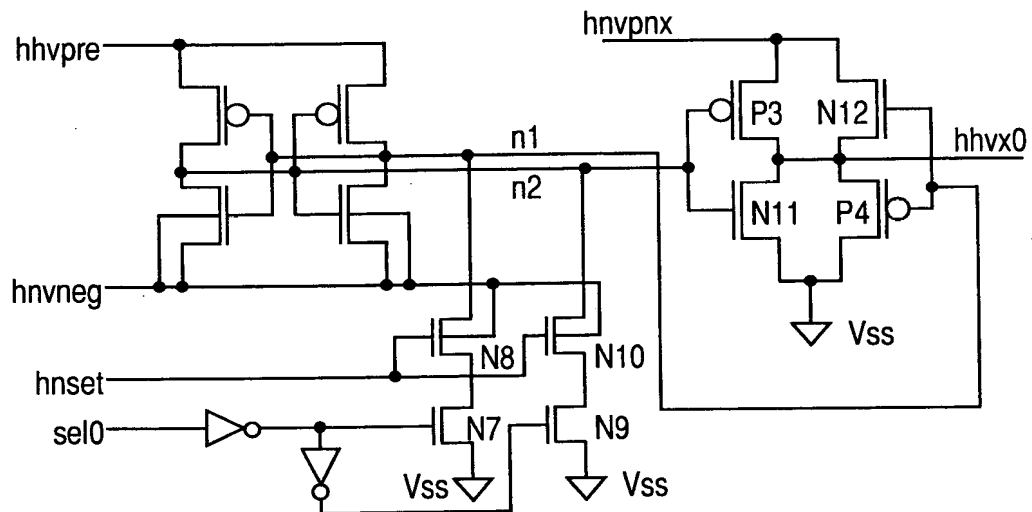


Fig.6

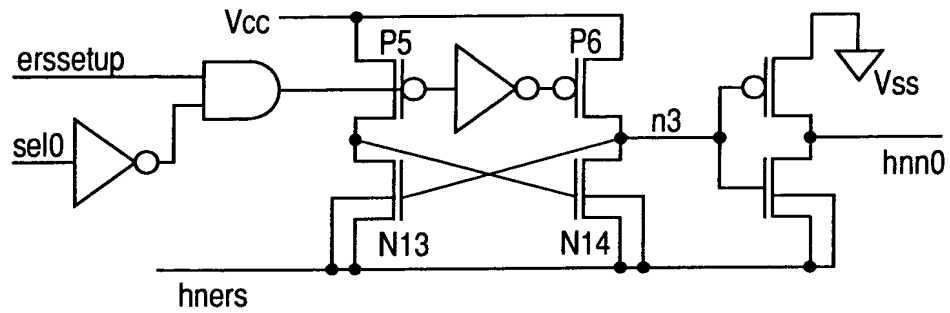


Fig.7

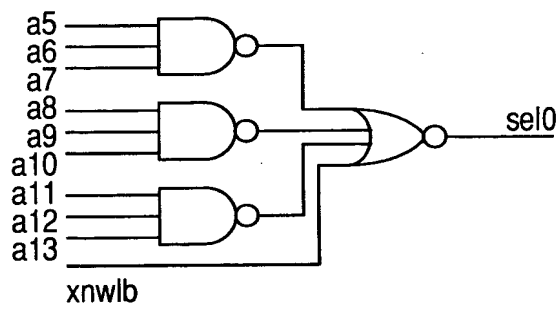
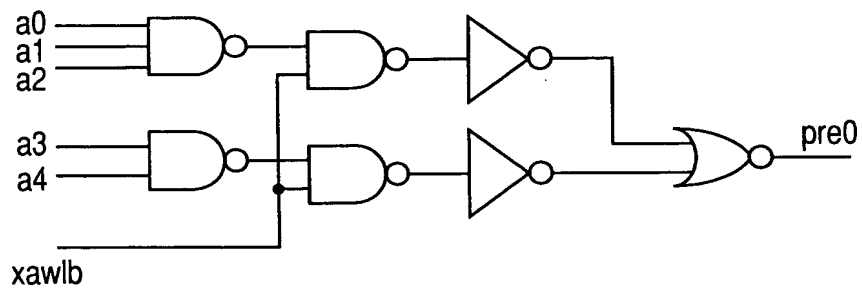


Fig.8



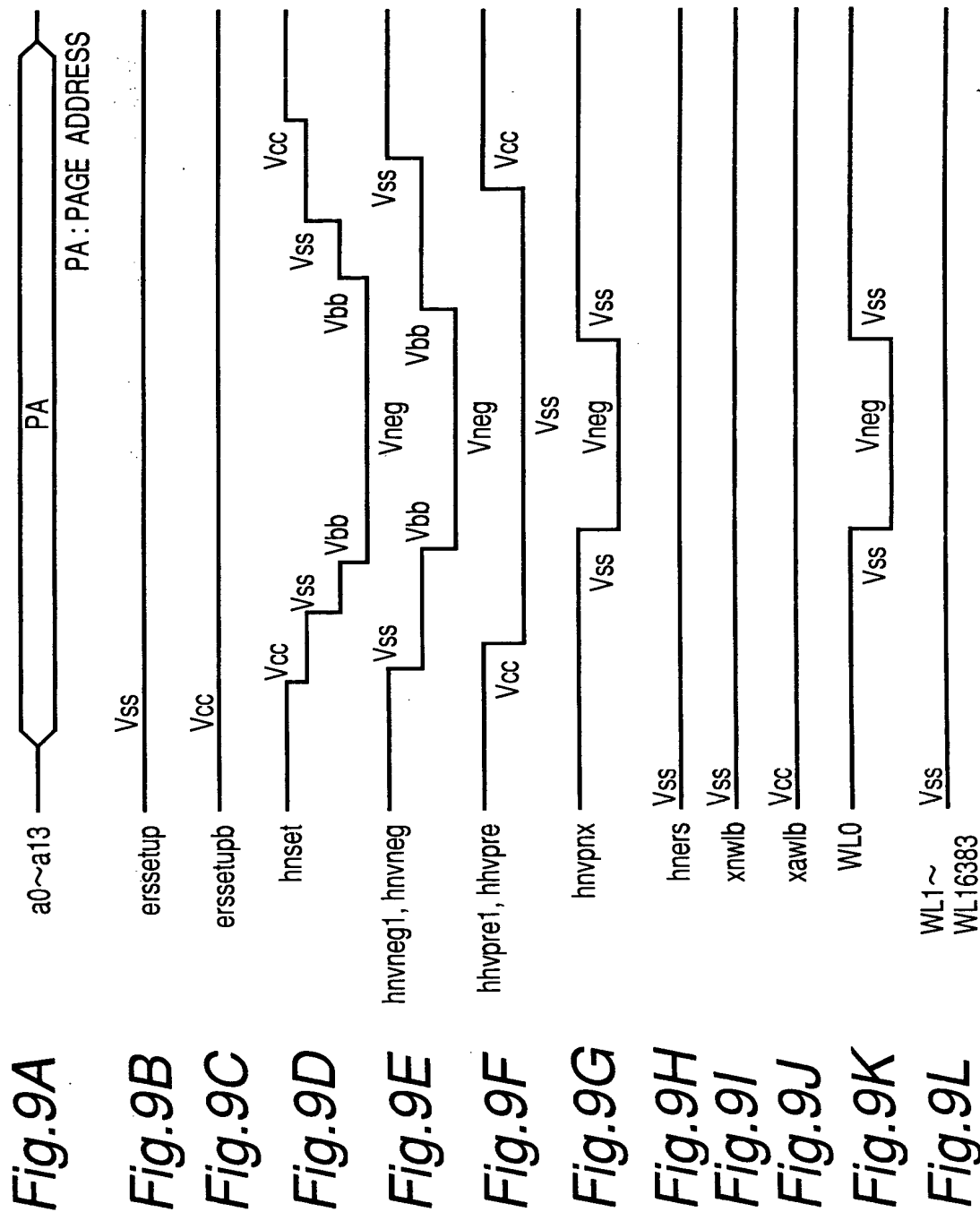


Fig. 10A

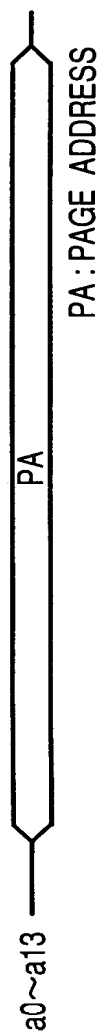


Fig. 10B



Fig. 10C



Fig. 10D



Fig. 10E



Fig. 10F



Fig. 10G

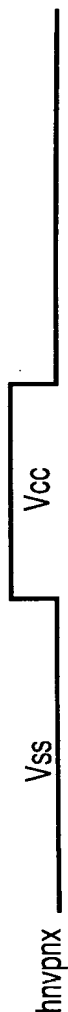


Fig. 10H



Fig. 10I



Fig. 10J



Fig. 10K

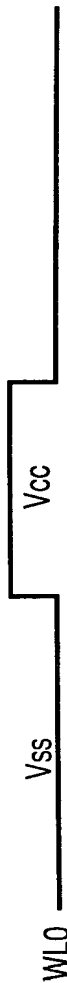


Fig. 10L



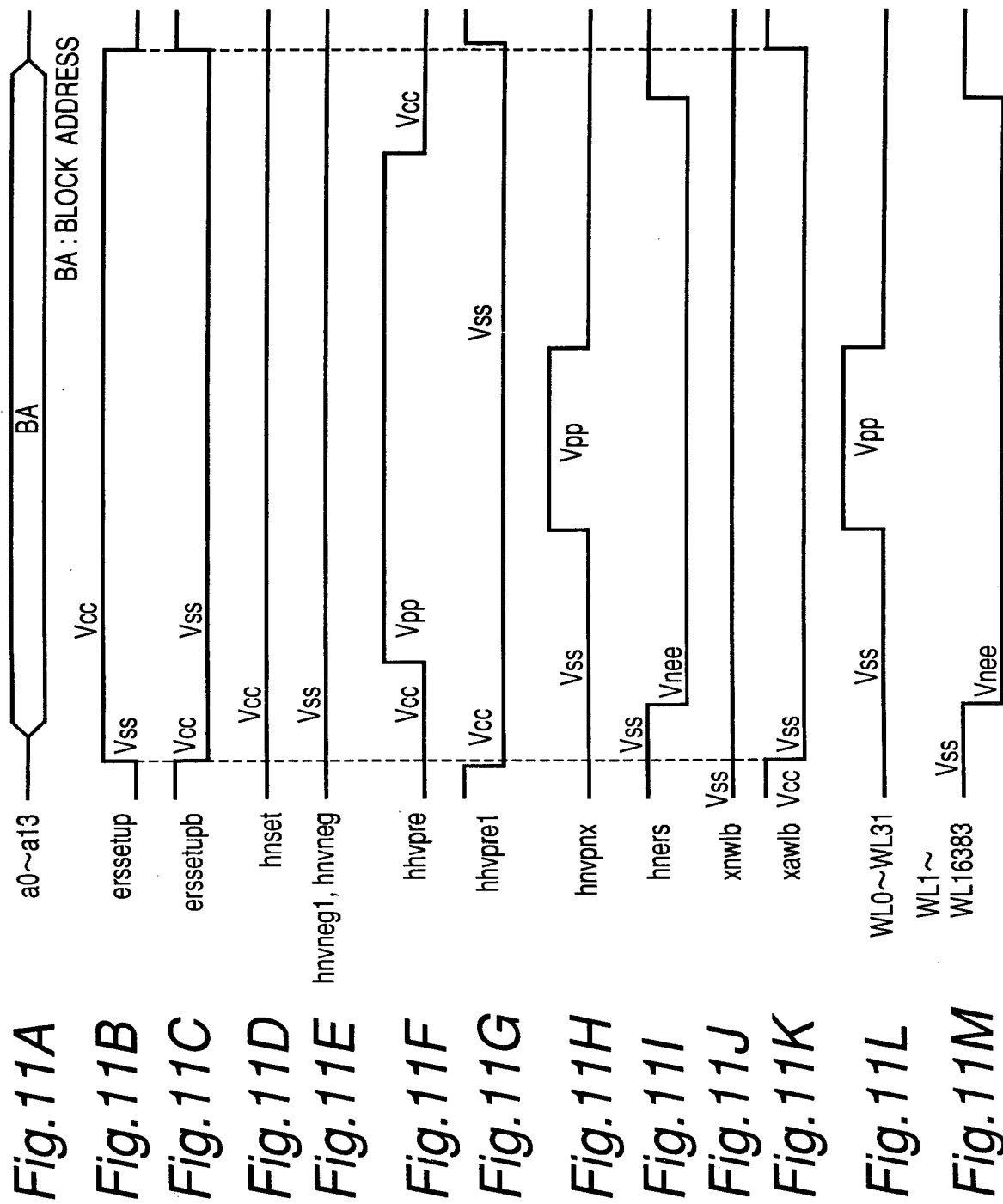


Fig. 12

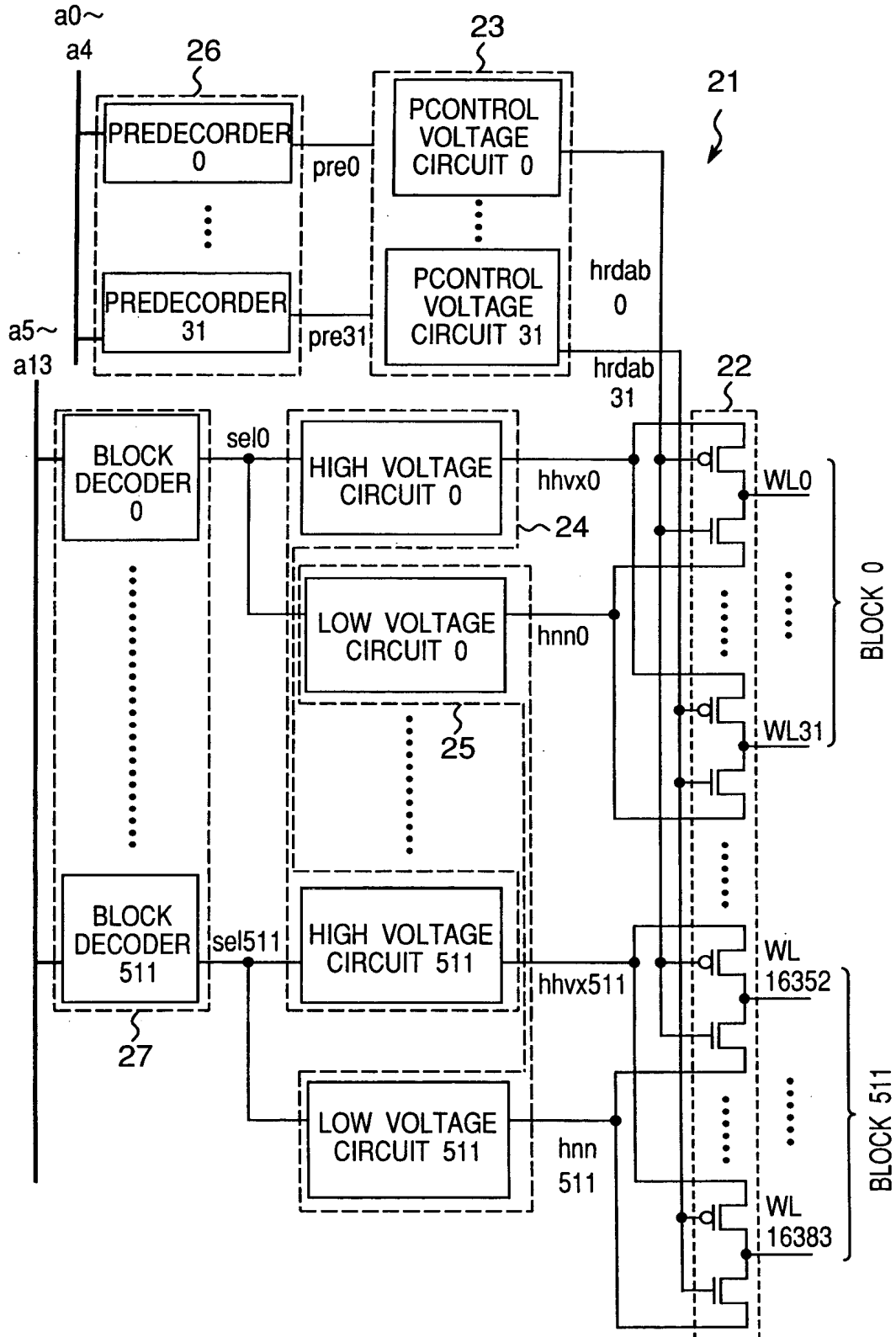


Fig. 13

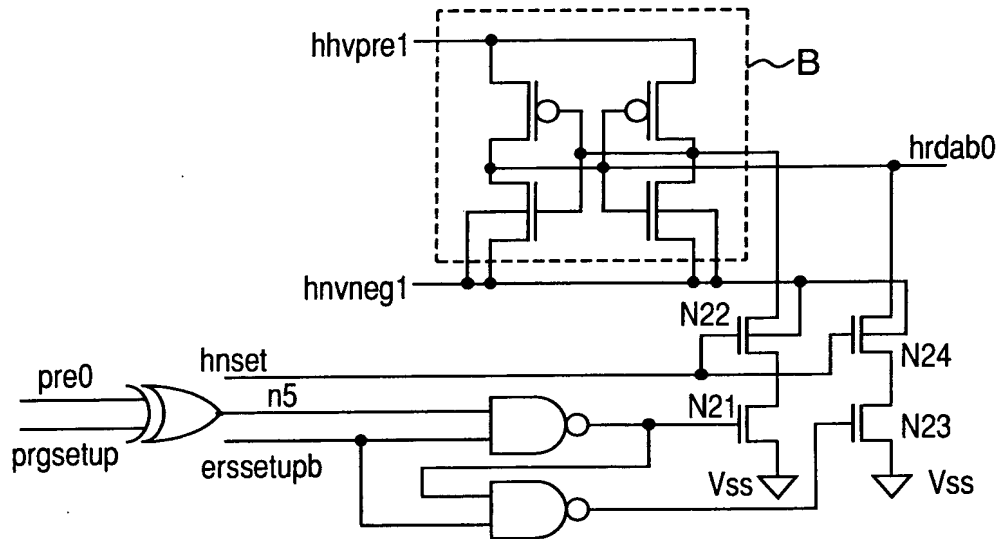


Fig. 14

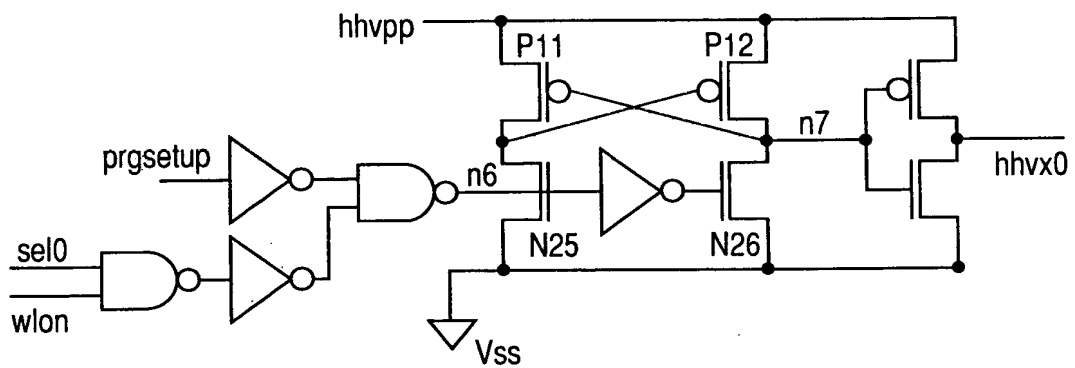
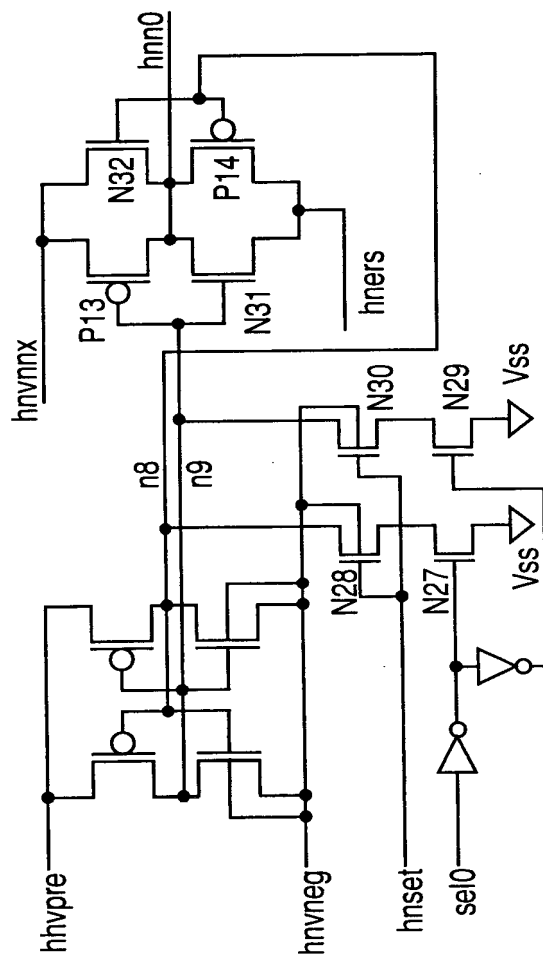


Fig. 15



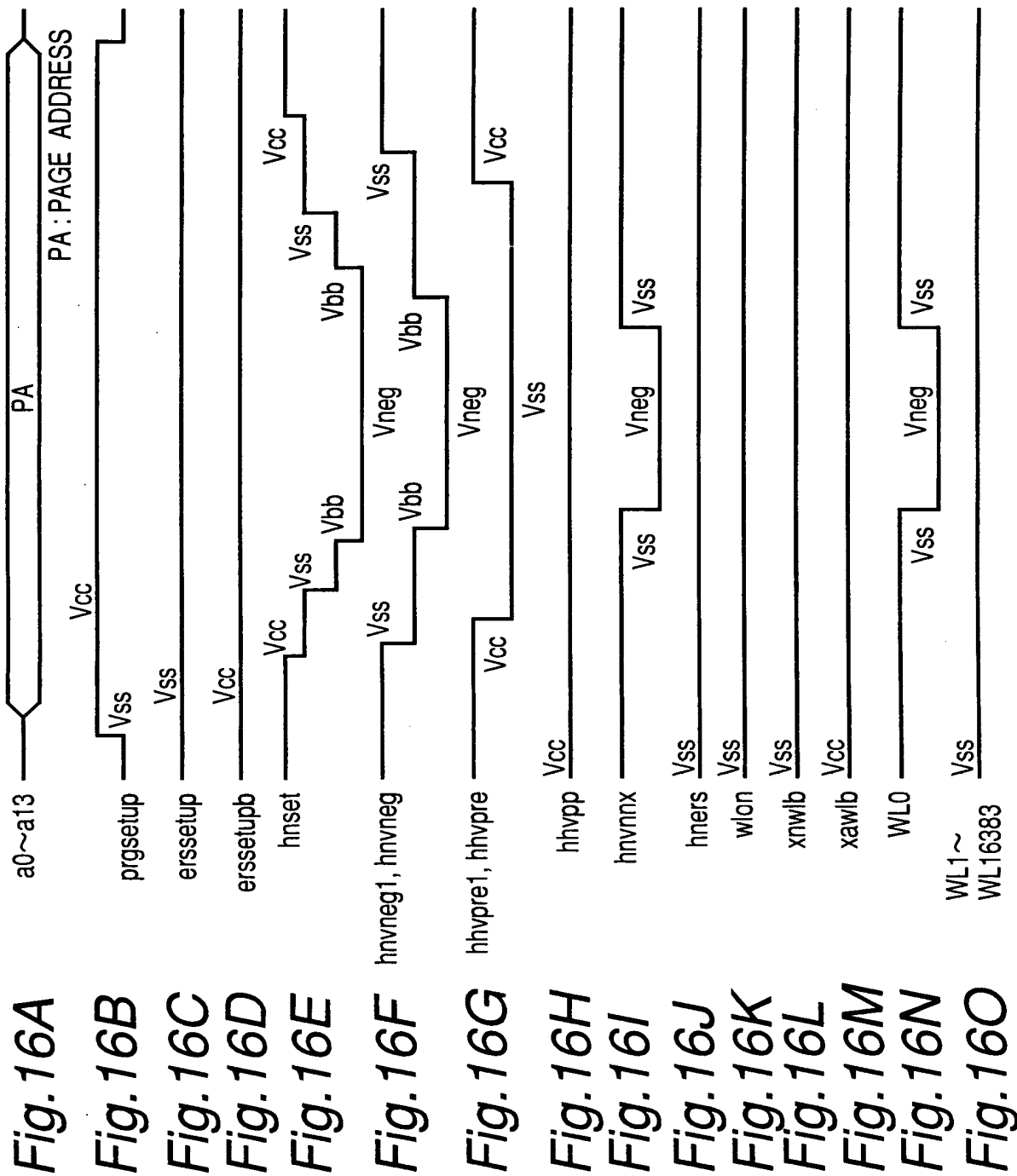


Fig. 17A



Fig. 17B



Fig. 17C



Fig. 17D



Fig. 17E



Fig. 17F



Fig. 17G



Fig. 17H



Fig. 17I



Fig. 17J



Fig. 17K



Fig. 17L



Fig. 17M



Fig. 17N

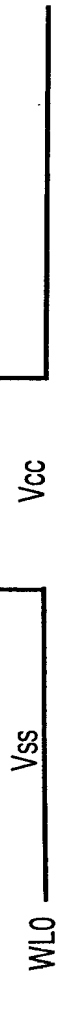


Fig. 17O



Fig.18A

a0~a13

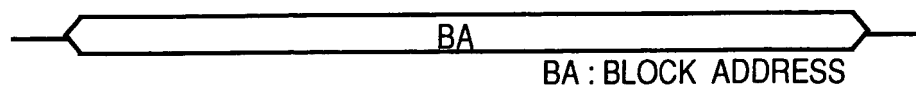


Fig.18B

prgsetup



Fig.18C

erssetup



Fig.18D

erssetupb



Fig.18E

hhvpp



Fig.18F

hvnvx



Fig.18G

hnrs



Fig.18H

wlon

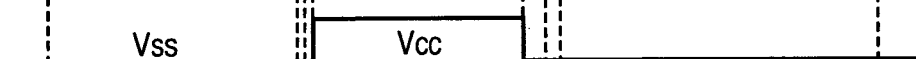


Fig.18I

xnwl

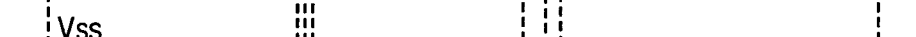


Fig.18J

xawl



Fig.18K

WL0~WL31

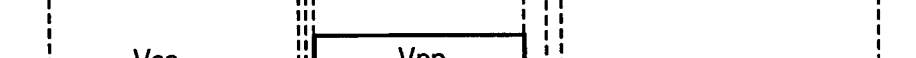


Fig.18L

WL32~WL16383



Fig.18M

hnset



Fig.18N

hvneg



Fig.18O

hhvpre

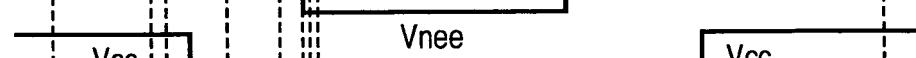


Fig.18P

hhvpre1

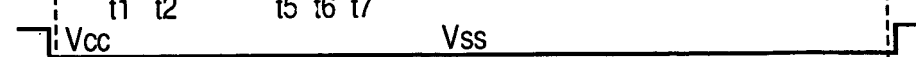


Fig.18Q

hvneg1



Fig.19

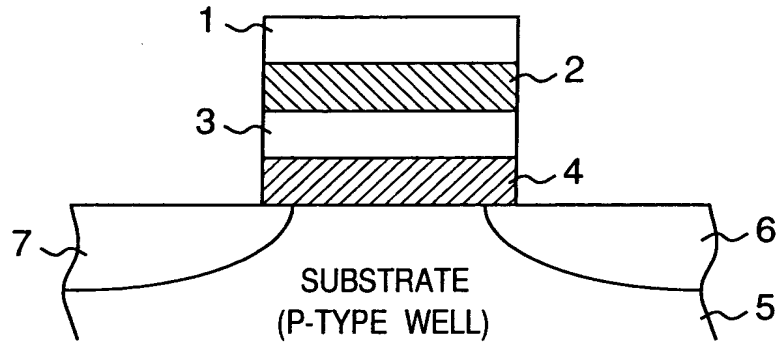
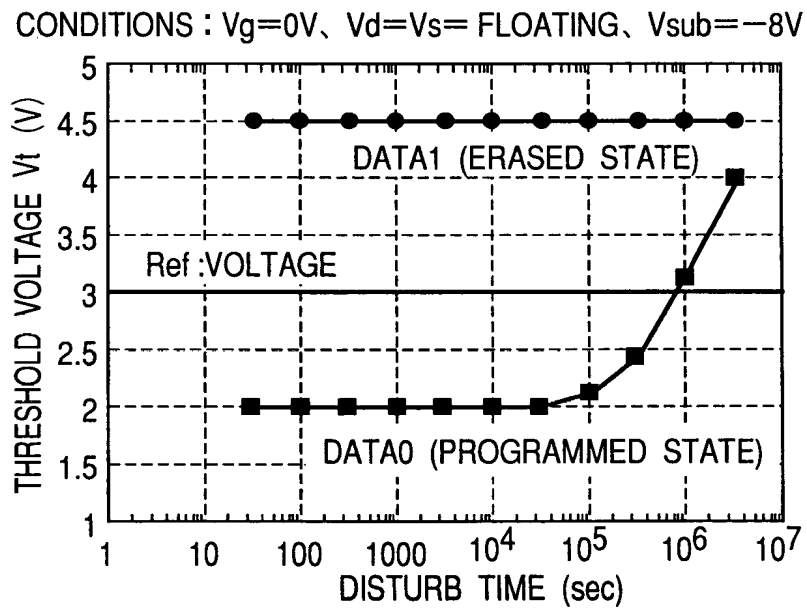


Fig.20 PRIOR ART



[illegible]